



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of

Avner Pierre BADEHI

Serial No.: 09/725,166

Filed: November 29, 2000

For: METHODS FOR PRODUCING
PACKAGED INTEGRATED CIRCUIT DEVICES
AND PACKAGED INTEGRATED CIRCUIT
DEVICES PRODUCED THEREBY

PATENT

Art Unit: 2813

Examiner: S. Geyer

RECEIVED
JUN - 6 2001
TECHNOLOGY CENTER 2800

RESPONSE

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the July 3, 2001 Office Action, Applicant elects the invention of Group I, claims 1-19, 30-32 and 35 without traverse

Prompt and favorable action is solicited.

Respectfully submitted,

May Birk

Mark S. Bicks
Reg. No. 28,770

ROYLANCE, ABRAMS, BERDO & GOODMAN, L.L.P.
1300 19th Street, N.W., Suite 600
Washington, D.C. 20036
(202) 659-9076

Dated: Aug 2, 2001